



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of

) Art Unit: 2822

Shunpei YAMAZAKI et al. ✓

) Examiner: M. Wilczewski

Serial No. 08/799,506 ✓

Filed: February 12, 1997 ✓

For: METHOD FOR PROCESSING ✓

SEMICONDUCTOR DEVICE APPARATUS ✓

FOR PROCESSING A SEMICONDUCTOR ✓

AND APPARATUS FOR PROCESSING

SEMICONDUCTOR DEVICE

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with  
The United States Postal Service with sufficient postage as First  
Class Mail in an envelope addressed to: Commissioner for Patents,  
Washington, D.C. 20231, on 12/17/02

**INFORMATION DISCLOSURE STATEMENT**

Commissioner for Patents  
Washington, DC 20231


Sir:

In accordance with the provisions of 37 C.F.R. 1.56 and 37 C.F.R. 1.97-1.99, Applicant submits herewith a Form PTO-1449 listing references known to Applicant and requests that these references be considered by the Examiner and made of record in the above identified application. Copies of the references listed are submitted herewith in accordance with 37 C.F.R. 1.98(a).

These documents are being filed concurrently with a *Request for Continued Examination (RCE)*, therefore no fee is believed to be required.

The Commissioner is hereby authorized to charge any fees connected with this filing which may be required now, or credit any overpayment to Deposit Account No. 50-2280.

Respectfully submitted,

  
Eric J. Robinson  
Reg. No. 38,285

Robinson Intellectual Property Law Office, P.C.  
PMB 955  
21010 Southbank Street  
Potomac Falls, Virginia 20165  
(571) 434-6789

RECEIVED  
DEC 26 2002  
TECHNOLOGY CENTER 2800

#57  
12/3/02